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PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Sung-Jin Park, et al.

Serial No.: 10/634,746

Filed: August 6, 2003

For: TURBO DECODING APPARATUS

: Group Art Unit: Not Assigned

: Examiner: Not Assigned

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. §§ 1.56 and 1.97-1.98, Applicants hereby transmit a European Search Report and copies of the documents listed in the attached Form PTO-1449, which may be deemed material to the examination of the above-identified application.

As indicated, all documents are in English or include English abstracts and therefore, no statement of relevance is required. Also, since the present Information Disclosure Statement is being filed prior to issuance of a first Office Action on the merits, no certification under 37 C.F.R. § 1.97(e) or fee under 37 C.F.R. § 1.17(b) is required.

The Examiner is requested to consider the attached documents in connection with the above-identified application and to return a copy of Form PTO-1449 to the Applicants with the Examiner's initials in the spaces provided.

Submission of this Information Disclosure Statement does not constitute an admission by the Applicants as to the materiality of the attached documents to the application, nor do the Applicants waive any right to challenge the validity of the documents as prior art should such action be deemed appropriate.

Respectfully submitted,



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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO. 45565	SERIAL NO. 10/634,746
APPLICANT Sun-Jin Park , et al.		
FILING DATE August 6, 2003		GROUP Not Assigned

U.S. Patent Documents

Published U.S. Patent Application

		Patent No.	Pub. Date	Patentee	Class	Sub-class	Filing Date

Foreign Patent Documents or Foreign Patent Applications

Other Documents (including Arthur, Title, Date, Pertinent Pages, etc.)

		Gross, Warren J., Gaudet, Vincent C., Gulak, P. Glenn, "Difference Metric Soft-Output Detection: Architecture and Implementation" pp. 1-8, figure 7, September 2001.
		Sklavos, N., Kitsos, P., Zervas, N., & Koufopavlou, O.; "A New Low Power and high Speed Bidirectional Shift Register Architecture", figures 1 and 2, July 2001.
		Masera, G., et al., "VLSI Architectures for Turbo Codes", IEEE Transactions on Very Large Scale Integration (VLSI) Systems, IEEE Inc., New York, vol. 7, no. 3, pp. 369-379, September 1999.
		"Bidirectional Shift Registers", February 1999.

***EXAMINER:** Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw Line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.